



## TriMedia Kit

The TriMedia Kit is a complete kit for development with the Philips TriMedia TM-1300 Media Processor for video, audio and telecoms applications.

The kit comprises all the components - software and hardware - needed to evaluate the TriMedia and to develop, test and debug applications.

The TriMedia is designed to implement real time applications that integrate video, audio and telecoms. The chip is a 166 MHz, 32 bit VLIW processor supported by a highly developed optimizing C/C++ compiler with device and software application libraries. The chip also has built in video, audio and telecoms I/O designed to run concurrently without loading the core processor.

There are two main components to the kit, each detailed in their own individual technical data sheet, and with further information available on the Internet as follows:

### TriMedia Kit components:

TM-1300 IREF:

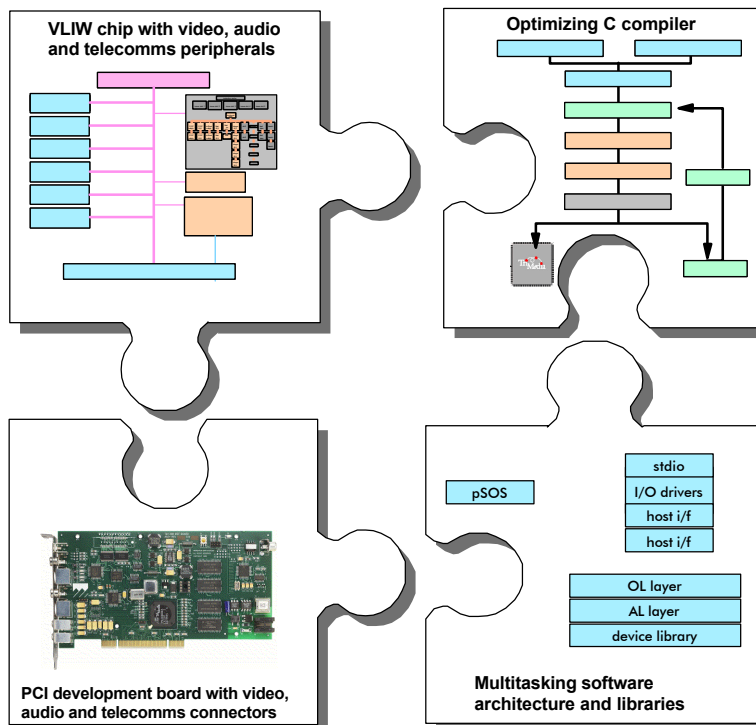
- PCI board with video, audio, telecoms I/O

[http://www.mds.com/products/tm-1300\\_tdk/index.htm](http://www.mds.com/products/tm-1300_tdk/index.htm)

Philips SDE:

- Software Development Environment with compiler

<http://www-us.semiconductors.philips.com/trimedia>

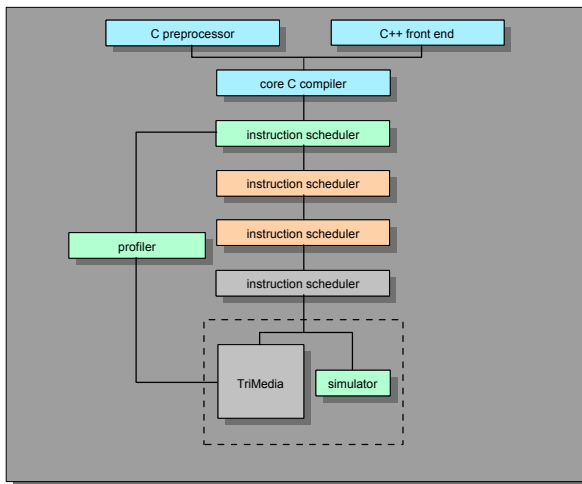
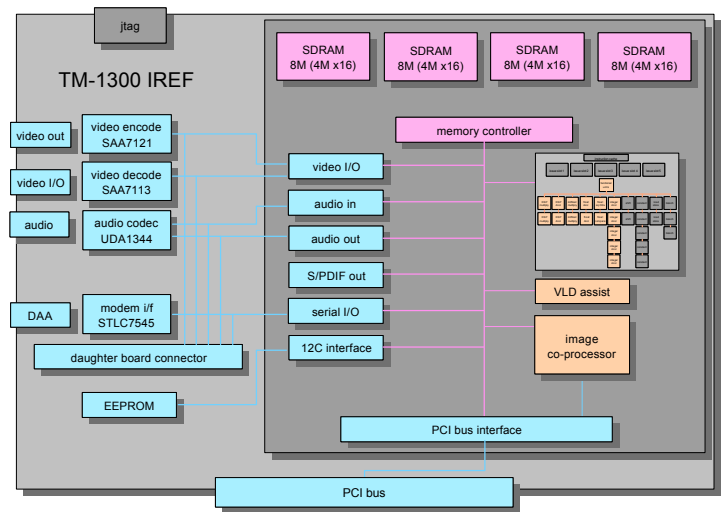


# TriMedia Developer's Kit

## The hardware

The TM-1300 IREF is a PCI board with a 166 MHz TM-1300 Media Processor (run at 166 MHz), and with video, audio and telecoms codecs. It is ready for immediate use with demonstration programs (supplied) that show every aspect of the TriMedia's use; and for development and testing of new software. Windows 9x and NT drivers are supplied.

The IREF board carries 32 Mbytes SDRAM. It also provides CVBS and S-Video format video I/O; stereo audio I/O; and a DAA (Data Access Arrangement) through a modem interface. A daughter board connector allows for custom I/O to be added.



## Software Development Environment

The Philips Software Development Environment (SDE) provides an optimizing C/C++ compiler; libraries of I/O device drivers and application modules; and a highly developed software architecture that supports easily reusable software modules.

The compiler includes a scheduler that produces efficient code to take advantage of the TriMedia's parallel VLIW architecture; and special custom operations that allow direct use from C of parallel instructions.

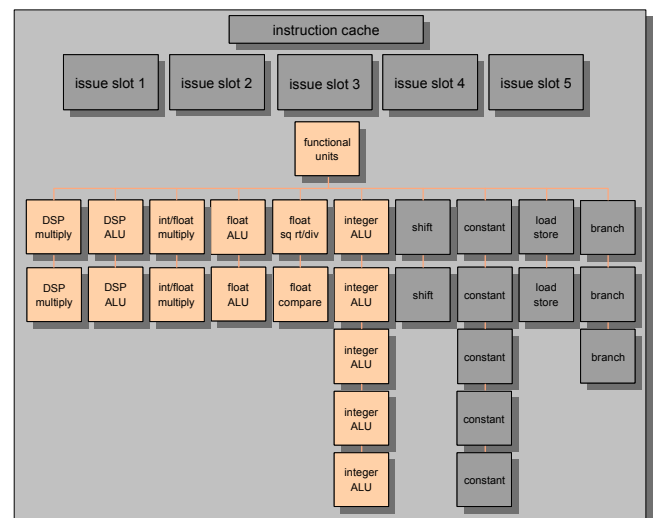
The SDE also includes tools for profiling of code.

## VLIW chip with multimedia I/O

The Philips TriMedia TM1300 is a 166 MHz VLIW chip with 27 functional units, 5 of which can be used in parallel at any time. It also has built in I/O peripherals to handle real time video, audio and telecoms interfaces.

There are two dedicated co-processors: an Image CoProcessor performs image scaling and filtering; and a Variable Length Decoder performs Huffman decoding (for MPEG video).

I/O peripherals work in the background, transferring data to and from TM1300 memory through DMA.



DS018 TM1300 TDK data sheet, draft rev B, June, 2001



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